

```
graph TD; 110((DATA 110)) --> 120[DATA SIGNAL GENERATOR 120]; 120 --> 130[DATA SIGNAL 130]; 130 --> 150[SIGNAL MODULATOR 150]; 122[CARRIER SIGNAL GENERATOR 122] --> 140[CARRIER SIGNAL 140]; 140 --> 150; 150 --> 160[MODULATED CARRIER SIGNAL 160]; 124[MASKING SIGNAL GENERATOR 124] --> 170[MASKING SIGNAL 170]; 160 --> 180[MASKED ENCODED SIGNAL 180]; 170 --> 180;
```

The diagram illustrates a signal processing system. It begins with a **DATA 110** input, which feeds into a **DATA SIGNAL GENERATOR 120**. This generator produces a **DATA SIGNAL 130**, which is then sent to a **SIGNAL MODULATOR 150**. Simultaneously, a **CARRIER SIGNAL GENERATOR 122** produces a **CARRIER SIGNAL 140**, also fed into the **SIGNAL MODULATOR 150**. The **SIGNAL MODULATOR 150** combines these signals to produce a **MODULATED CARRIER SIGNAL 160**. Separately, a **MASKING SIGNAL GENERATOR 124** produces a **MASKING SIGNAL 170**. Finally, the **MODULATED CARRIER SIGNAL 160** and the **MASKING SIGNAL 170** are combined to produce the final **MASKED ENCODED SIGNAL 180**.

FIG. 1

002290" 6E6E0960

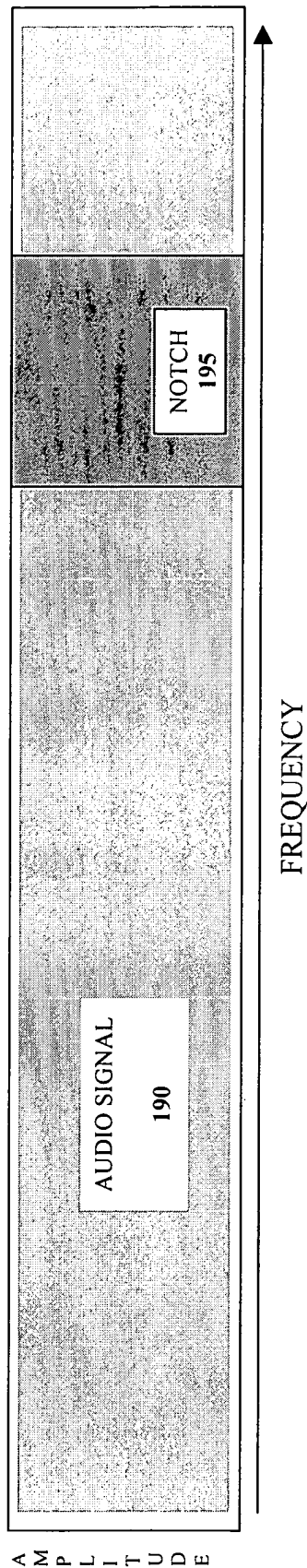
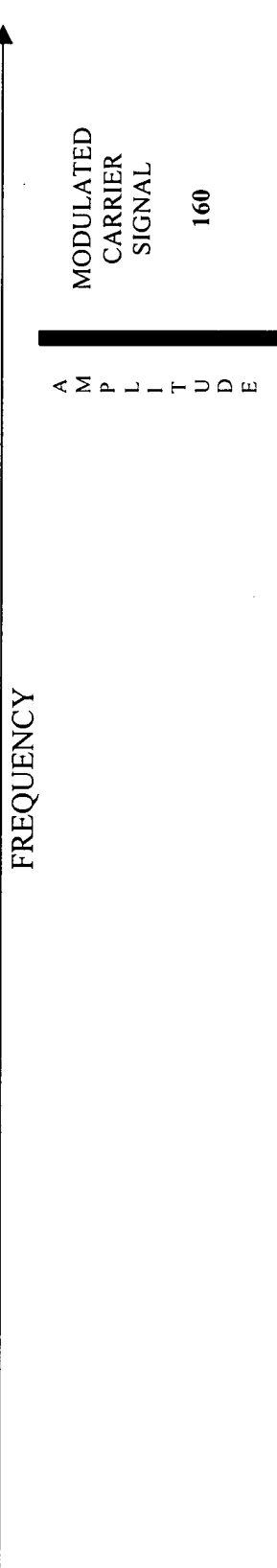
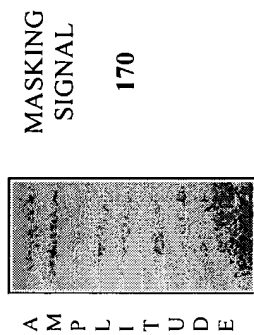


FIG. 3

FREQUENCY

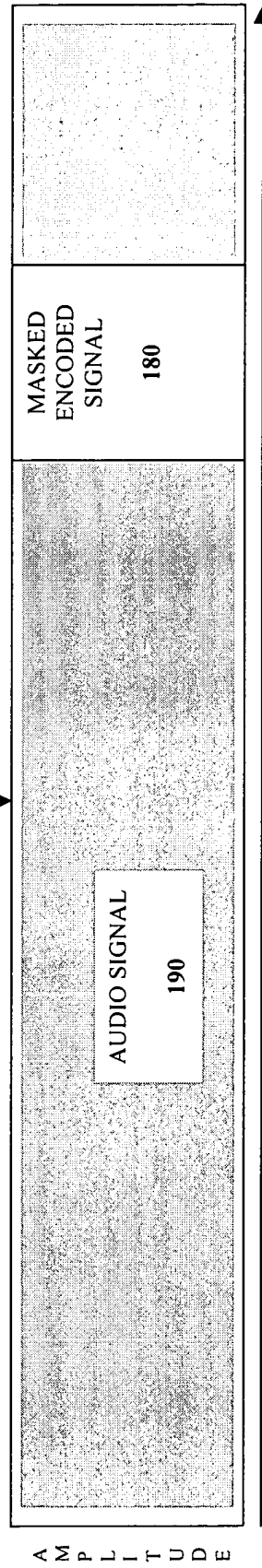
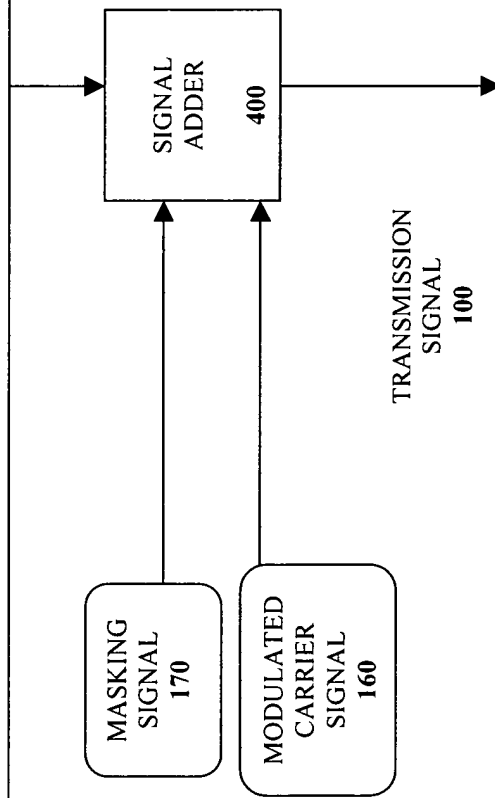
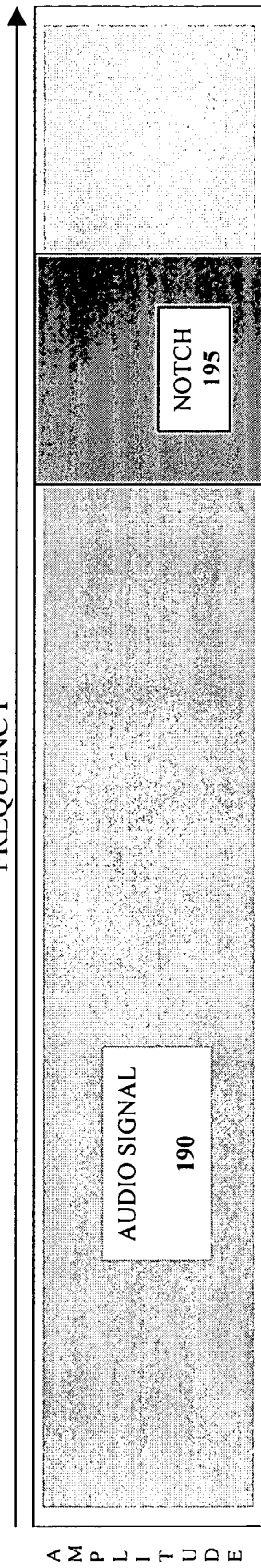


FIG. 4

002290" 6660960

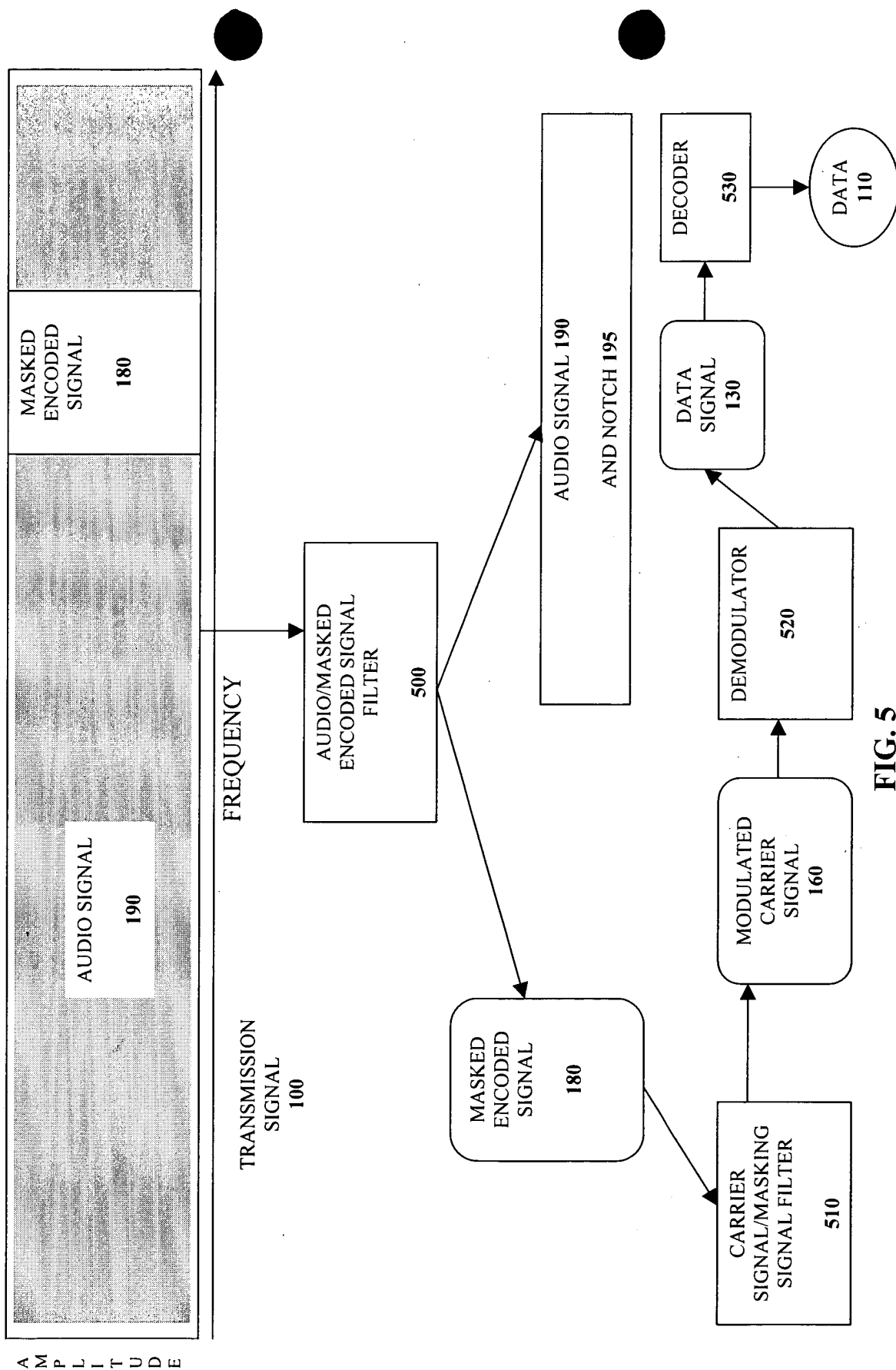


FIG. 5